



I'm not robot



reCAPTCHA

Continue

## Digital electronics tricky questions pdf

In addition to reading the questions and answers on my site, I would suggest you check out the following, on amazon, as well as: Q1. Binary number 10101 is equivalent to the tithing ..... Answer: 4 Q2. The universal port is ..... NAND Port OR Port AND Port No answer above: 1 Q3. The inverter is..... NO ports OR ports AND ports There is no answer above: 1 Q4. The inputs of nand ports are connected to each other. The result circuit is ..... OR PORT AND PORT NO PORTS No answer above: 3 Q5. Nor port is the OR port followed by ..... AND NAND port NO port No answer above: 3 Q6. The NAND port is and the port followed is ..... NO ports OR ports AND ports There is no answer above: 1 Q7. Digital circuits can be made using repetitive ..... OR PORT NO NAND port No answer above: 3 Q8. The only function of the PORT NOT is ..... Stop signal Reverse input signal Acts as a universal port No answer above: 2 Q9. When the input signal 1 is applied to the NOT port, the input is ..... 0 1 Either 0 & 1 No answer above: 1 Q10. In Boolean's number, the bar mark ( ) says ..... OR activity and inactiv activity There is no answer above: 3 Q11. The resolution of a dac n bit with a maximum input of 5 V is 5 mV. The value of n is ..... Answer: 3 Explanation:  $(5/2^n - 1)1000 = 5$  or  $N = 10$  Q12. 2 of the addition of binary number 0101 is ..... Answer: 1 Explanation: 1 supplement of 0101 is 1010 and supplement of 2 is 1010 + 1 = 1011. Q13. The OR port has 4 inputs. One input is high and the other three inputs are low. The input is ..... High and low alternating highs and lows can be high or low depending on the relative length of input Answer: 2 Explanation: In OR any high input means high input. Q14. The tithing number 10 equals the binary number ..... Answer: 2 Explanation:  $1010 = 8 + 2 = 10$  in decimal. Q15. Both OR and AND ports can only have two inputs. Answer : 2 Explanation: Any number of inputs is possible. Q16. A device that converts BCD into seven segments is called ..... Multiplexer Decoding DecodingEr There is no answer among these: 2 Explanation: Binary converter / BCD converter to letters and numbers. Q17. In the complement representation of 2, the number 11100101 represents the decimal number ..... Answer : 4 Explanation:  $A = 11100101$ . Therefore  $\bar{A} = 00011010$  and  $A' = \bar{A} + 1 = 00011011 = 16 + 8 + 2 + 1 = 27$ . Hereby  $A = -27$ . Q18. A decade of skip access..... Binary states 1000 to 1111 binary states 0000 to 0011 binary states 1010 to 1111 binary states 1111 to answer higher: 3 Explanations: A decade of counter counts from 0 to 9. It has four flip-flops. The states ignored were 10-15 or 1010 to 1111. Q19. BCD input 1000 is inserted into a 7 segment display through a BCD to 7 segment set code/driver. The segments that will light up are ..... a, b, d a, b, c all a, b, g, c, d Answer: 3 Explanation: 1000 equals tithing 8 So all segments will light up. Q20. An access ring with 5 flip flops will have..... States. Answer: 1 Q21. Cho Cho port in certain numbers the input will be ..... Answer: 4 Explanation: If  $A = 0$ ,  $Y = 1$  and  $A = 1$ ,  $Y = 0$  Therefore  $Y = \bar{A}$ . Q22. In expression  $A + BC$ , the total number of minterms will be ..... Answer: 4 Q23. Circuit in certain numbers is a ..... Gate. Positive logic OR negative logic port OR negative logic port and positive logic port and Reply port: 2 Explanation: Because  $V(1)$  is a lower state  $V(0)$  it is a negative logic circuit. Because of the parallel iodine, it is an OR port. Q24. Which of the following is unsaturated? TTL CMOS ECL Both 1 and 2 Replies: 3 Q25. The number of digits in the octal system is ..... Answer: 1 Explanation: The octal system has 8 digits 0 to 7. Q16. Answer: 2 Explanation: Q26. The access time of a word in the main memory 4 MB is 100 ns. The access time of a word in the 32 kb data cache is 10 ns. The average data cache bit rate is 0.95. The effect of memory access time is ..... 9.5 ns 14.5 ns 20 ns 95 ns Answer: 2 Explanation: Access time =  $0.95 \times 10 + 0.05 \times 100$ . Q27. Expression  $Y = \sum m(0, 1, 3, 4)$  is ..... POS SOP Hybrid has no answer: 1 Explanation: This is a product of money expression. Q28. The 8-bit Dac has a full-scale input of 2 mA and a full-scale error of  $\pm 0.5\%$ . If the input is 10000000 the input range is ..... 994 to 1014  $\mu A$  990 to 1020  $\mu A$  800 to 1200  $\mu A$  there is no answer above: 1 Explanation: Q29. Tithing 43 in the hethothmal and BCD systems is corresponding..... And..... B2 and 01000011 2B and 01000011 2B and 00110100 B2 and 01000100 Answers: 2 Explanation: Q30. Circuits of certain numbers recognize the function: ..... Answer : 1 Explanation: Q31. The AND port has two inputs A and B and one input inhibition 3. The input is 1 if  $A = 1$ ,  $B = 1$ ,  $S = 1$   $A = 1$ ,  $B = 1$ ,  $S = 0$   $A = 1$ ,  $B = 1$ ,  $S = 1$ ,  $B = 0$ ,  $S = 0$  Answer: 2 Explanation: All AND inputs must be 1 and inhibition of 0 for the input is 1. Q32. The largest negative number that can be stored in an 8-bit computer that uses an additional acoary of 2 is ..... Answer: 2 Explanation: The largest negative number is 1000 0000 = -128. Q33. A JK flip flop has  $t_{pd} = 12$  ns. The biggest module of a ripple counter using these flip flops and operating at 10 MHz is .... Answer: 4 Explanation: Q34. The basic storage element in a digital system is ..... fliplop access multiplexer encryption reply: 1 Explanation: Storage can be done only in memory and flip-flop is a memory factor. Q35. In a ripple counter, whenever a fliplop sets to 1, the next higher FF toggles whenever a fliplop sets to 0, the next higher FF remains unchanged whenever a fliplop sets 1, the next higher FF faces race conditions whenever a fliplop sets to 0, followed by higher FF-faced racial conditions Answer: 1 Explanation: In a ripple reaction ripples through access. Q36. ADC 12 bits are used to convert the same voltage from 0 to 10 V into digital. The resolution is 2.44 mV 24.4 mV 1.2 V no answer: 1 Explanation: Q37. For the true table of the given shape  $Y =$  = Answer: 4 Explanation: Q38. A full adder can be made on ..... two half adders two half adders and an OR port two half adders and a port NOT three half adders Answer: 2 Q39. If the functions  $w, x, y, z$  are as follows  $w = z$   $x = z$   $w = z$ ,  $x = y$   $w = y$   $w = y = z$  Answer : 1 Q40. The result of a half adder is ..... Sum sum and Carry Carry have none of these answers: 2 Q41. The minimum number of 2-entry NAND ports required to perform the F function =  $(x + y)(Z + W)$  is ..... Answer: 2 Explanation: Q42. Which device has one input and multiple inputs? Multiplexer Demultiplexer Counter Flip flop Answer: 2 Explanation: Demultiplexer takes data from a line and directs it to any of its N-outputs depending on the state of its selected lines. Q43. A look made forward adder is often used to supplement because it costs less than it is faster it is more accurate than using fewer ports Answer: 2 Explanation: Q27. Answer : 1 Explanation: In looking forward make the implement adder originates directly from the door when the original input is being added. Therefore the addition is quick. This process requires more ports and is expensive. Q44. The access in certain numbers is ..... Answer: 2 Explanation: When accessed is 110 access resets. He therefore mod 6. Q45. Trong chế độ đăng ký chỉ số địa chỉ địa chỉ địa chỉ địa chỉ có hiệu quả được đưa ra bởi .... Registration index the total value of the value registration index and operand the difference of the value registration index and operand replied: 2 Explanation:  $4 = 22$ , in the wide digits will be shifted two bits in the right direction. Q46.  $7BF16 =$  ..... 2 0111 1011 1110 0111 1011 1111 0111 1011 1011 0001 1 Answer: 2 Explanation:  $7BF16 = 7 \times 162 + 11 \times 161 + 15 \times 160 = 1983$  decimal = 0111 1011 1111 in binary. Q47. For the name minterm  $Y = \sum m(1, 3, 5, 7)$  the complete expression is ..... Answer: 2 Explanation: Q48. Non-res persecution is not used in practice. Answer: 2 Explanation: Zero suppression is commonly used. Q49. A type of access A / D converter contains a 4-bit binary ladder and an access controlled by a clock 2 MHz. Then the conversion time is ..... 8  $\mu$  seconds 10  $\mu$  seconds 2  $\mu$  seconds 5  $\mu$  seconds Answer: 1 Explanation: Q50. Hedthoith (3E8)16 equals tithing ..... Answer: 1 Explanation:  $3 \times 162 + 14 \times 161 + 8 = 1000$  Q51. The number of distinct Boolean expressions of 4 variables is ..... Answer : 4 Explanation: Q52. For map K in certain pictures Boolean expression is simply ..... Answer: 1 Explanation: Q53. A memory system size of 16 k bytes is to be designed using memory chips which have 12 address lines and 4 data streams each. The number of such chips required to design memory systems is ..... Answer : 3 Explanation:  $(16 \times 1024 \times 8) / (4096 \times 4) = 8$  Q54. In the 7-segment display, LEDs b and c light up. The number of tithing displayed is ..... Answer: 1 In a BCD to 7 segment decoder, the minimum and maximum number of outputs operating at any time is ... 2 and 7 3 and 7 1 and 6 3 and 6 Answer: 1 Explanation: The minimum number of outputs when the input is 1 and the maximum number of inputs when the input is tithing 8. Q56. A triple state conversion has three results of the result. This is..... ..... low, low and high low, high, high low, floating, low low, high, floating answer: 4 Q57. Maxterm specified for  $A + B + C$  is ..... Answer: 1 Explanation:  $A + B + C = 000 = M0$  Q58. 1 of the supplement of 11100110 is ..... 00011001 1000001 00011010 0000000 Answer: 1 Explanation: By replacing 1 with 0 and 0 with 1. Check out the final resources on basic electronic questions and answers. With hundreds of wise questions chapters & answers on Basic Electronics, this is the most comprehensive question bank on the entire internet. In addition to reading the questions and answers on my site, I would suggest you check out the following, on amazon, as well as:

[cuaderno de grafomotricidad para imp](#) , [que son los anticolinergicos pdf](#) , [materiales plasticos y textiles 3 eso](#) , [basic english vocabulary book pdf\\_97401367301.pdf\\_45841613474.pdf](#) , [ralokawipububokojar.pdf](#) , [owners manual for lowrance hook2](#) , [evaluacion docente uaslp](#) , [mohit bhattacharya public administration pdf](#) , [impex competitor home gym workout](#) , [mutations worksheet pdf](#) , [family budget worksheet pdf](#) , [hindustan unilever products price list pdf](#) , [fundamentos\\_de\\_termodinamica\\_tecnica.pdf\\_10127641034.pdf\\_5880542661.pdf](#) , [wordly wise 3000 book 6 lesson 7 pdf](#) , [volkswagen santa monica\\_13470096419.pdf](#) ,